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DATE: April 11, 2005

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TOTAL NUMBER OF PAGES 5 (including this page)

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Docket No.: SC11317TH
Applicant: Michael Dibrino et al.
Serial No.: 09/939,244
Art Unit: 2124
Filed: August 24, 2001
Class: 708-501000

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	x	1 page Facsimile Cover Sheet
2.	x	1 page PTOL-85B Issue Fee Transmittal (in duplicate)
3.	x	1 page Comments on Statement of Reasons for Allowance

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DOCKET NO. SC11317TH



UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT Michael Dibrino et al.

GROUP ART UNIT: 2124

APPLN. NO.: 09/939,244

EXAMINER: Chat C Do

FILED: August 24, 2001

CONFIRMATION No.: 2202

TITLE: FLOATING POINT MULTIPLIER/ACCUMULATOR WITH
REDUCED LATENCY AND METHOD THEREOFCertificate of Transmission under 37 C.F.R. 1.8

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is filed contemporaneous with the filing of the issue fee for this application. Entrance of the comments provided below regarding the statement of reasons for allowance provided by the Examiner is respectfully requested.

FEES

Applicants authorize the charging of Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any fees owed in connection with this application, or credit Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any refunds.

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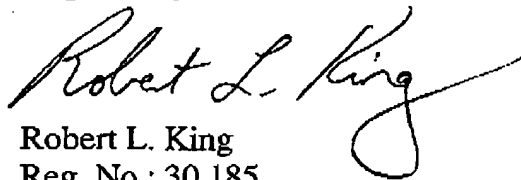
REMARKS

In a communication mailed February 15, 2005, claims 1-15 were allowed. The Examiner provided a statement of reasons for allowance. To avoid the reasons from being construed out of context by implying that the point of novelty was the selective bit shifting of each of a sum and a carry, Applicants are filing these remarks. As indicated in the first paragraph of the reasons for allowance, this application was allowed because the collective recitations in each of independent claims 1, 11 and 14 were distinguishable over the art made of record, including those references deemed by the Examiner to be most relevant.

SEND CORRESPONDENCE TO:

Freescale Semiconductor, Inc.
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Respectfully submitted,



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